

Do not enter
ML 12/6/06

In the Claims:

Please amend the claims as follows:

1. (Currently Amended) A method for maximizing CPU performance in a multiprocessor comprising ~~computer implemented instructions, comprising:~~
organizing data elements stored in a shared resource designed to support data manipulation functions;
~~instructions for~~ executing write operations in local memory in an arbitrary order and at any time prior to storing a pointer from an existing element of a said shared resource stored on a computer readable medium to a new element of said shared resource element, wherein said pointer is stored in said shared resource;
~~instructions for~~ explicitly indicating a set of write operations to non-local memory be conducted in a specified order; and
~~instructions for~~ forcing execution of said write operations to non-local memory to precede said storing said pointer from said existing element of said shared resource to said new element of said shared resource in response to said indicating.
2. (Currently Amended) The method of claim 1, further comprising ~~instructions for~~ assigning first and second registers of a CPU for storing associated first and second instruction addresses.
3. (Currently Amended) The method of claim 2, further comprising ~~instructions for~~ providing a third instruction referencing said registers.
4. (Original) The method of claim 3, wherein said third instruction specifies ordering between said first and second instructions.
5. (Original) The method of claim 4, wherein said third instruction indicates said first instruction's execution attaining a first specified state of execution prior to said second